

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Rule 53(b) Divisional Application of S.N. 08/970,212:

**Shunji NAKAMURA**

Serial No.: NEW

Filed: **December 6, 2001**

For: **SEMICONDUCTOR MEMORY DEVICE AND METHOD FOR FABRICATING  
THE SAME**

Group Art Unit: 2811 (Expected)

Examiner: NADAV D. (Expected)



11/2  
8-26-02  
Payton

**INFORMATION DISCLOSURE STATEMENT**  
**PURSUANT TO 37 CFR 1.97(b)**

Commissioner for Patents  
Washington, D.C. 20231

December 6, 2001

Sir:

This Information Disclosure Statement is being filed in order to comply with Applicant's duty of disclosure under 37 CFR 1.56. The documents listed on the Form PTO-1449 were made of record in parent application Serial No. 08/970,212 and grandparent application Serial No. 08/660,324.

The Commissioner is authorized to charge our Deposit Account No. 01-2340 for any fee which is deemed by the Patent and Trademark Office to be required to effect consideration of this statement.

Respectfully submitted,

ARMSTRONG, WESTERMAN, HATTORI,  
McLELAND & NAUGHTON

Sadao Kinashi  
Attorney for Applicant  
Registration No. 48,075

Attorney Docket No. 960456B  
1725 K Street, N.W., Suite 1000  
Washington, D.C. 20006  
Tel: (202) 659-2930  
Fax: (202) 887-0357  
Enclosures: PTO-1449